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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 08/530,661  
Filing Date ..... September 20, 1995  
Inventor ..... Brent Keeth et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2503  
Examiner ..... Unassigned  
Attorney's Docket No. .... MI22-356  
Title: Semiconductor Memory Circuitry

PRELIMINARY AMENDMENT

To: Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)  
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In the Claims

1. (Amended) A 64M semiconductor memory device comprising:  
a semiconductor die encapsulated in a package, the package having  
an encapsulating body and electrically conductive interconnect pins  
extending outwardly from the body;  
a total of [no more than] from 64,000,000 to 68,000,000 functional  
and operably addressable memory cells arranged in multiple memory  
arrays formed on the die, the individual functional and operably  
addressable memory cells occupying area on the die within the memory  
arrays, the occupied area of all functional and addressable memory cells  
on the die having a total combined area which is no greater than 53  
mm<sup>2</sup>; and